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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

K7102

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAJ	EXAMINER
10037436	12/31/2001	711	305	2185	

**APPLICANTS: Dodd James; Khandekar Narendra;
710 2181 K. Dang

**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed	<input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO
35 USC 119 conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no	5038-184
Verified and Acknowledged Examiners's initials		
TITLE : Memory bus termination with memory unit having termination control		

U.S. DEPT. OF COMM./PAT & TM-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
		PREPARED FOR ISSUE	
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